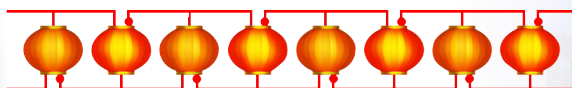


ASYNC 2023

JULY 16-19, 2023
BEIJING, CHINA



28TH IEEE INTERNATIONAL SYMPOSIUM ON ASYNCHRONOUS CIRCUITS AND SYSTEMS

ABOUT CONFERENCE

The International Symposium on Asynchronous Circuits and Systems (ASYNC) will be held during July 16-19, 2023 in Beijing, China, which provides a high quality forum for researchers and engineers from academia and industry to present their latest insights and results in Asynchronous VLSI designs. The conference is hosted by Tsinghua University. This is the first time for the conference to be held in China. Asynchronous circuits are event-driven, whose benefits include low power and high energy efficiency. Asynchronous design plays an important role in massive core distributed systems, low power and high energy efficiency circuits and systems, neuromorphic circuits and systems and so on. Authors are invited to submit their papers on all aspects of asynchronous designs.



CONFERENCE COMMITTEE ///

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- Hong CHEN, Tsinghua University, Beijing, China

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- Laurent FESQUET, Grenoble Institute of Technology, France
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CALL FOR PAPERS ///

The topics included but not limited to below:

- Asynchronous pipelines, architectures, CPUs, and memories;
- Asynchronous ultra-low power systems, energy harvesting and mixed-signal/analogue circuits and systems;
- Asynchrony in emerging technologies, including bio, neural, nano, and quantum computing;
- CAD tools for asynchronous design, synthesis, analysis, and optimization;
- Formal methods for verification and performance/power analysis;
- Test, security, fault tolerance, and radiation hard design;
- Asynchronous variability-tolerant design, resilient design, and design for manufacturing;
- Asynchronous design for neural networks and machine learning applications;
- Circuit designs, case studies, comparisons, and applications;
- Mixed-timed circuits, clock domain crossing, GALS systems, Network-on-Chip, and multi-chip interconnects;
- Hardware implementations of asynchronous models and algorithms, asynchronous techniques in clocked designs, and elastic and latency-tolerant synchronous design;
- Circuit designs, case studies, comparisons, and applications.

Submission Website:

<https://easychair.org/conferences/?conf=ieeeeasync2023>

SUBMISSION GUIDELINE ///

We invite you to submit **6-10 page regular papers** or **4-page short regular papers** with original scientific work relevant to ASYNC, in IEEE conference format (doublecolumn, 10pt or larger). Author information must be omitted from the manuscripts. Accepted papers must be presented and will be published in the Symposium Proceedings and the IEEE Xplore Digital Library.

We also encourage you to submit **1-2 page papers for demo/poster/idea** with a demo/poster abstract or "fresh ideas" to try out live. These go through a lightweight review. Accepted papers must be presented and will be distributed as handouts at the Symposium.

We solicit **1-2 page papers from Industry** on state-of-the-art integration of asynchronous designs to existing or emerging technologies. These must follow the format of a regular paper, but will go through a separate light-weight review process. Accepted papers must be presented and will be published in the Symposium Proceedings and the IEEE Xplore Digital Library.

Word Template(double-column):

https://www.async2023.org/Template_No_Copyright_Info.docx
LaTeX Template:

https://www.async2023.org/LaTeX_template.zip

IMPORTANT DATES ///

PAPER TYPE	ITEMS	DEADLINE
Regular Papers	Abstract Registration	14 March,2023
	Full Paper Submission	14 March,2023
	Notification of Acceptance	18 April, 2023
	Final Version	09 May, 2023
Industrial Papers (Tool & Demos) Fresh Idea Papers Posters	Paper Submission	30 April, 2023
	Notification of Acceptance	14 May, 2023
	Final Version	28 May, 2023

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